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## INFORMATION DISCLOSURE STATEMENT BY APPLICANT

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**Complete if Known**

Application Number	09/706,154
Filing Date	11/03/2000
First Named Inventor	Mario Nemirovsky
Art Unit	2183
Examiner Name	Shane Gerstl
Attorney Docket Number	P3816

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Examiner Signature	<i>E. Ch</i>	Date Considered	7/23/05
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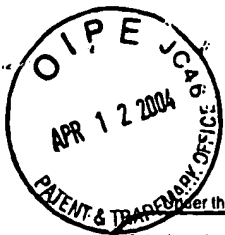
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NON PATENT LITERATURE DOCUMENTS			
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ES		DIEFENDORFF, KEITH, Compaq Chooses SMT for Alpha, Microprocessor Report, <a href="http://www.mdronline.com/mpr/h/19991206/131601.html">http://www.mdronline.com/mpr/h/19991206/131601.html</a> , December 6, 1999	
ES		DIEFENDORFF, KEITH, Jalapeno Powers Cyrix's M3, Microprocessor Report, <a href="http://www.mdronline.com/mpr/h/19981116/121507.html">http://www.mdronline.com/mpr/h/19981116/121507.html</a> , November 16, 1998	
ES		EGGERS et al., Simultaneous Multithreading: A Platform for Next-Generation Processors, September 1997, pp 12-19, IEEE Micro	
ES		SLATER, MICHAEL, Rise Joins x86 Fray with mP6, Microprocessor Report, <a href="http://www.mdronline.com/mpr/h/19981116/121501.html">http://www.mdronline.com/mpr/h/19981116/121501.html</a> , November 16, 1998	
ES		BECKER et al., The PowerPC 601 Microprocessor, October 1993, pp 54 - 68, IEEE Micro	

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